

AUDIO AMP for 16" to 24"

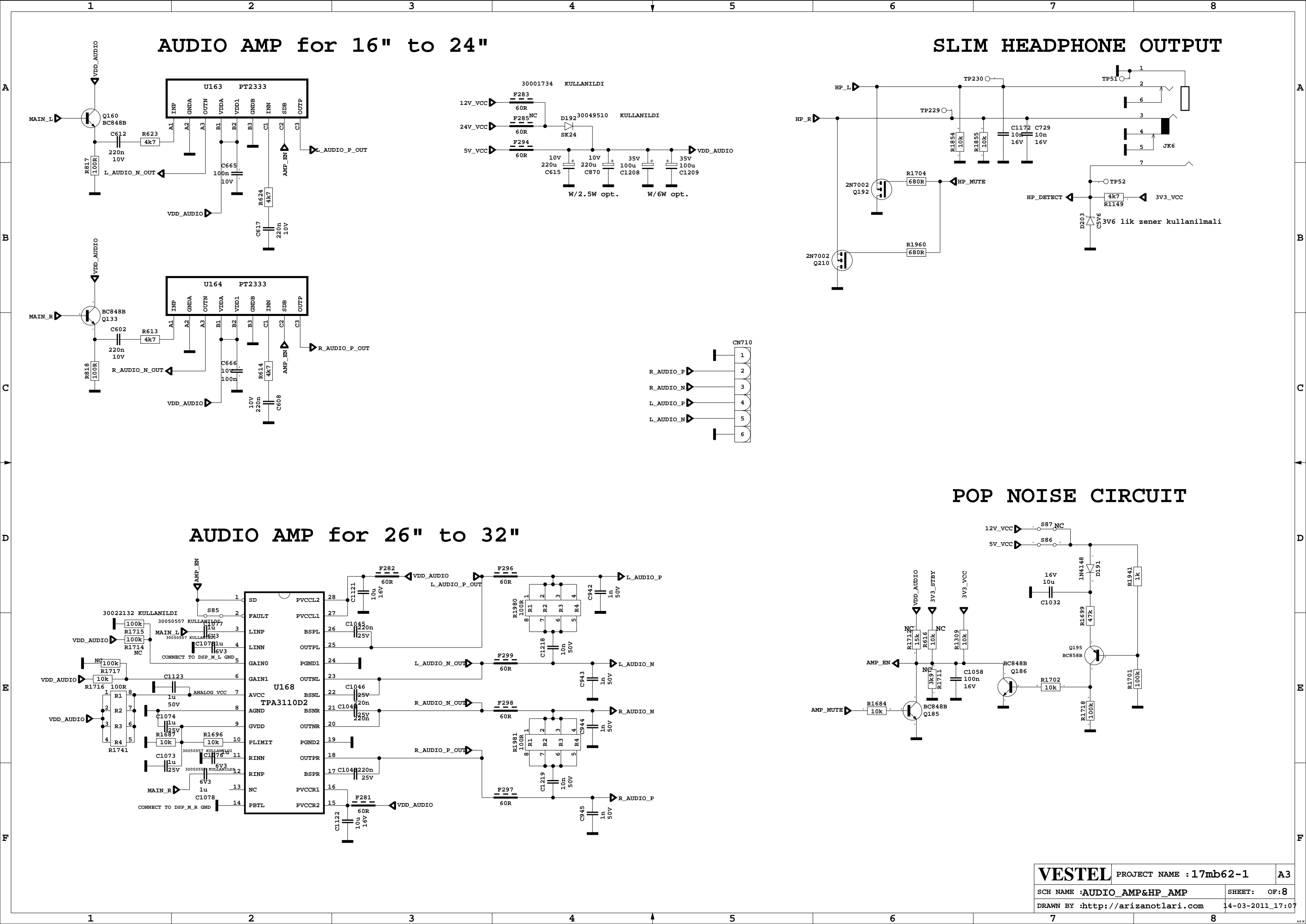
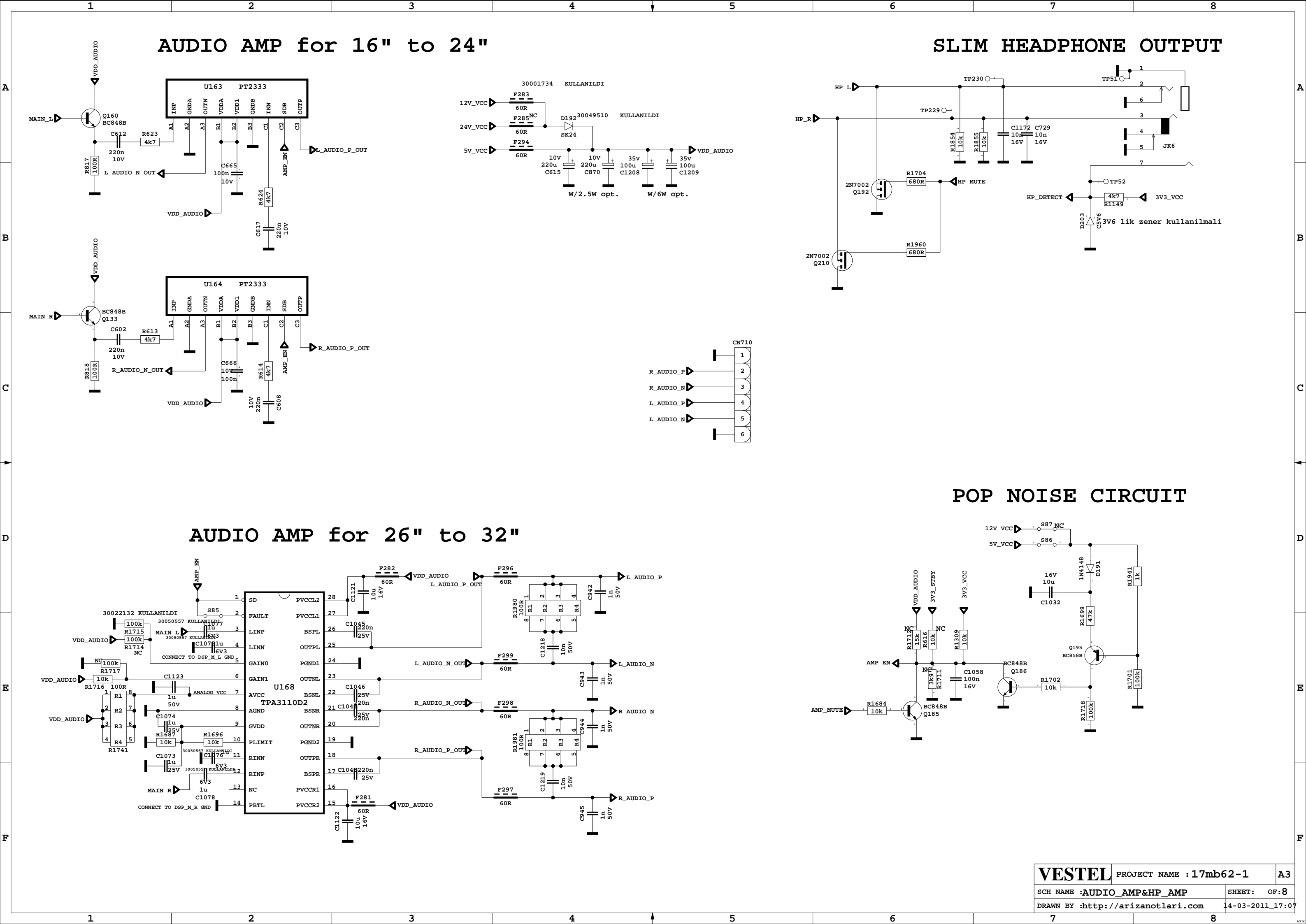
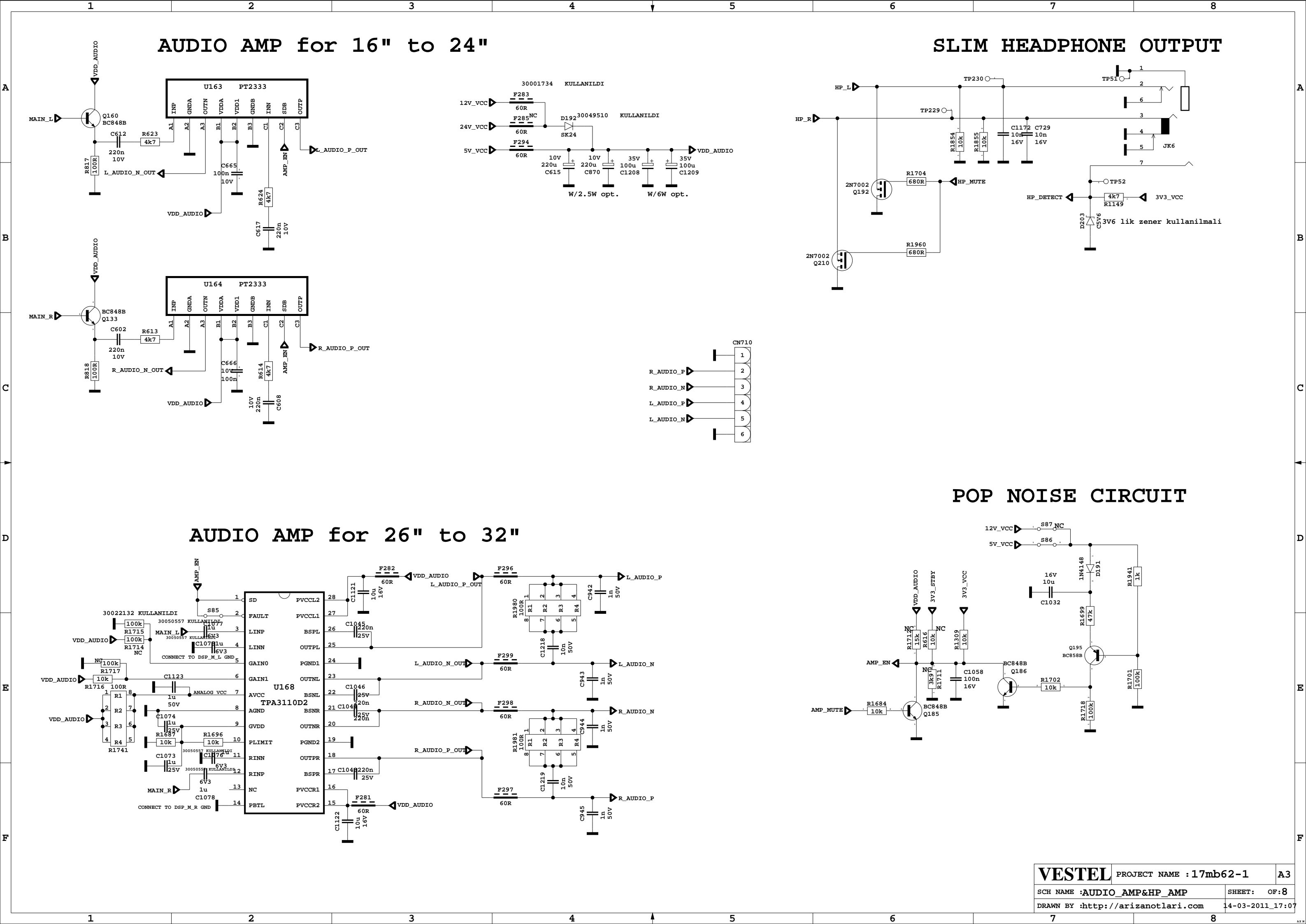
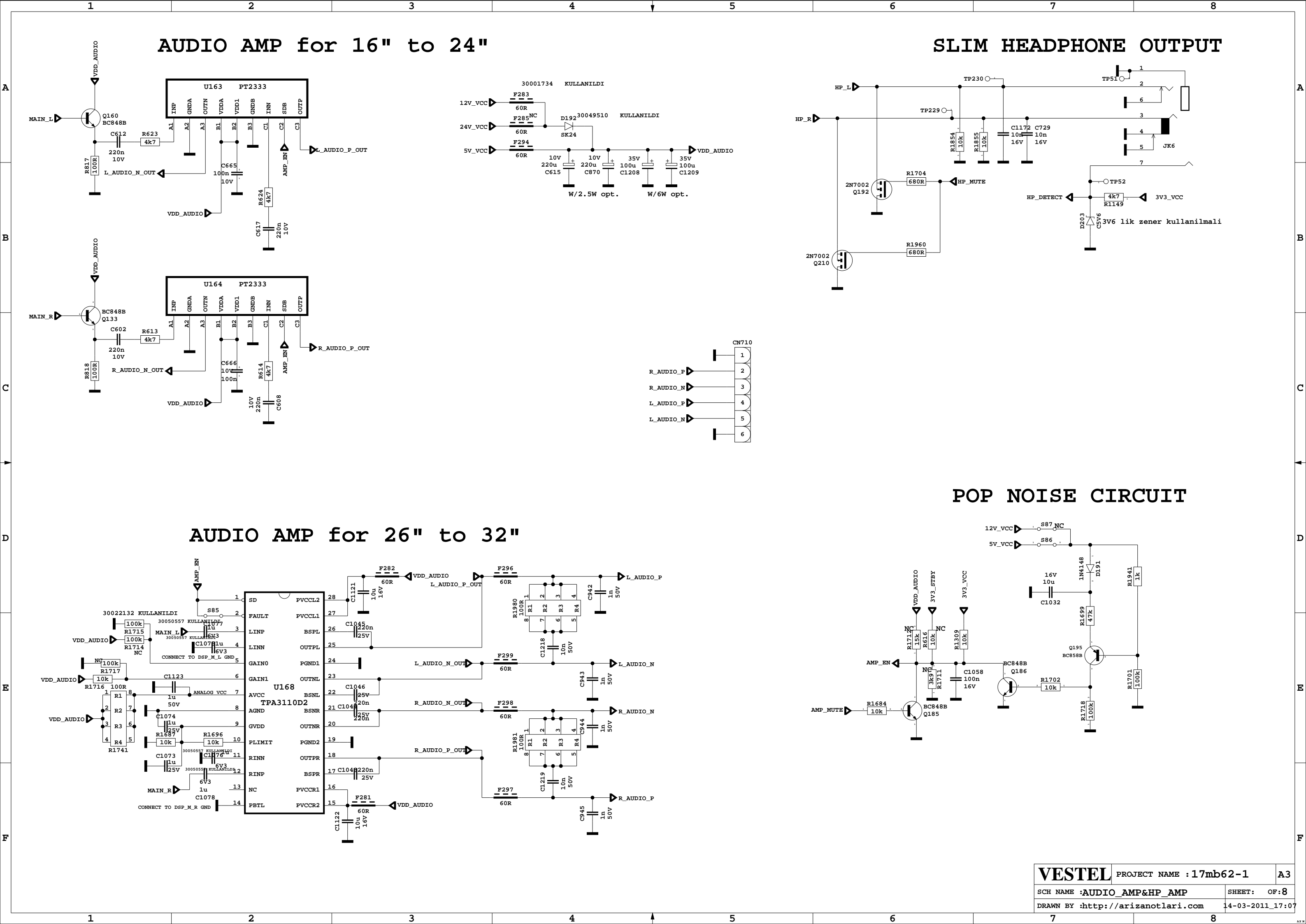
SLIM HEADPHONE OUTPUT

AUDIO AMP for 26" to 32"

POP NOISE CIRCUIT

SCH NAME	PROJECT NAME	SHEET	OF
:AUDIO_AMP&HP_AMP	:17mb62-1	8	8

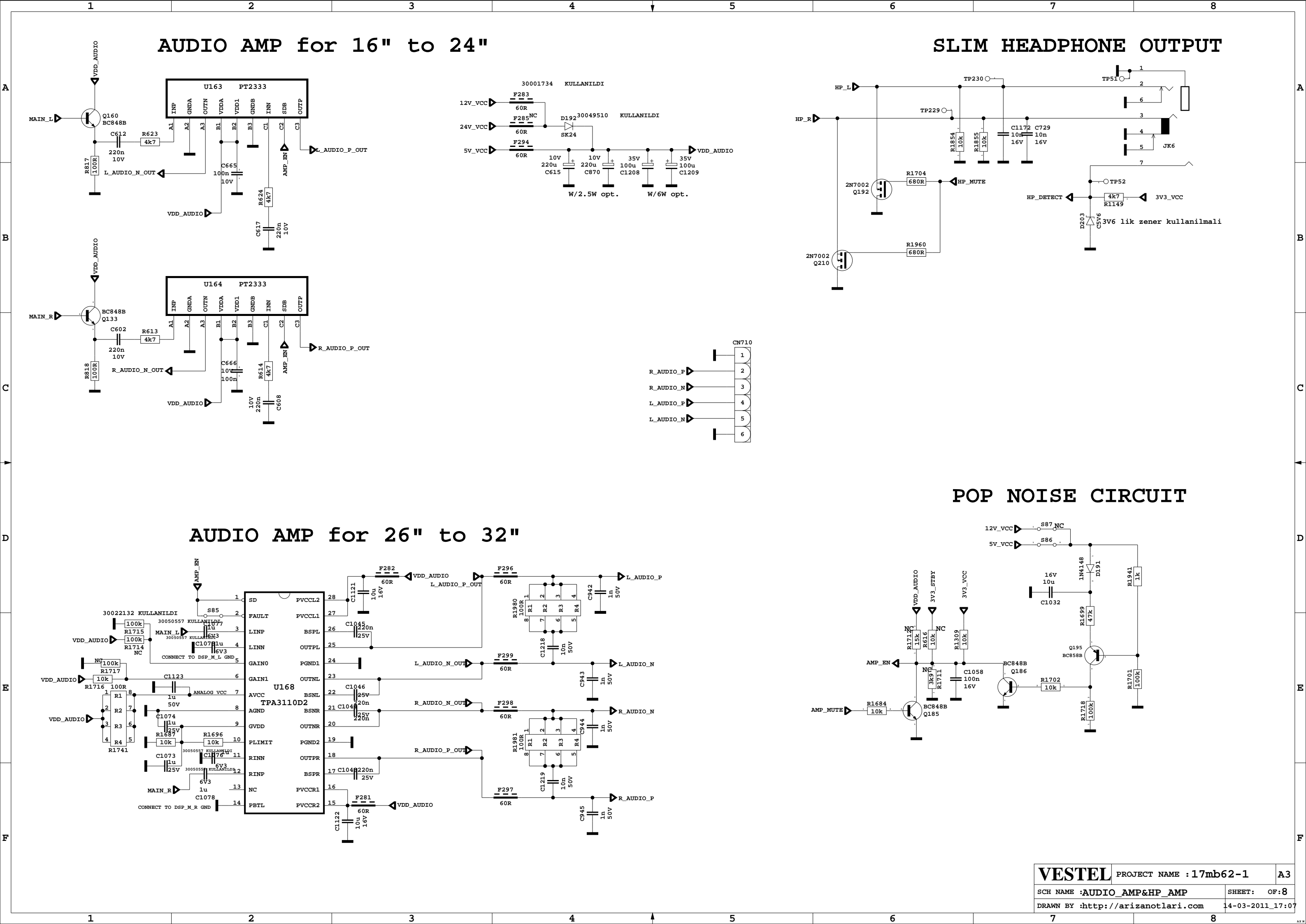
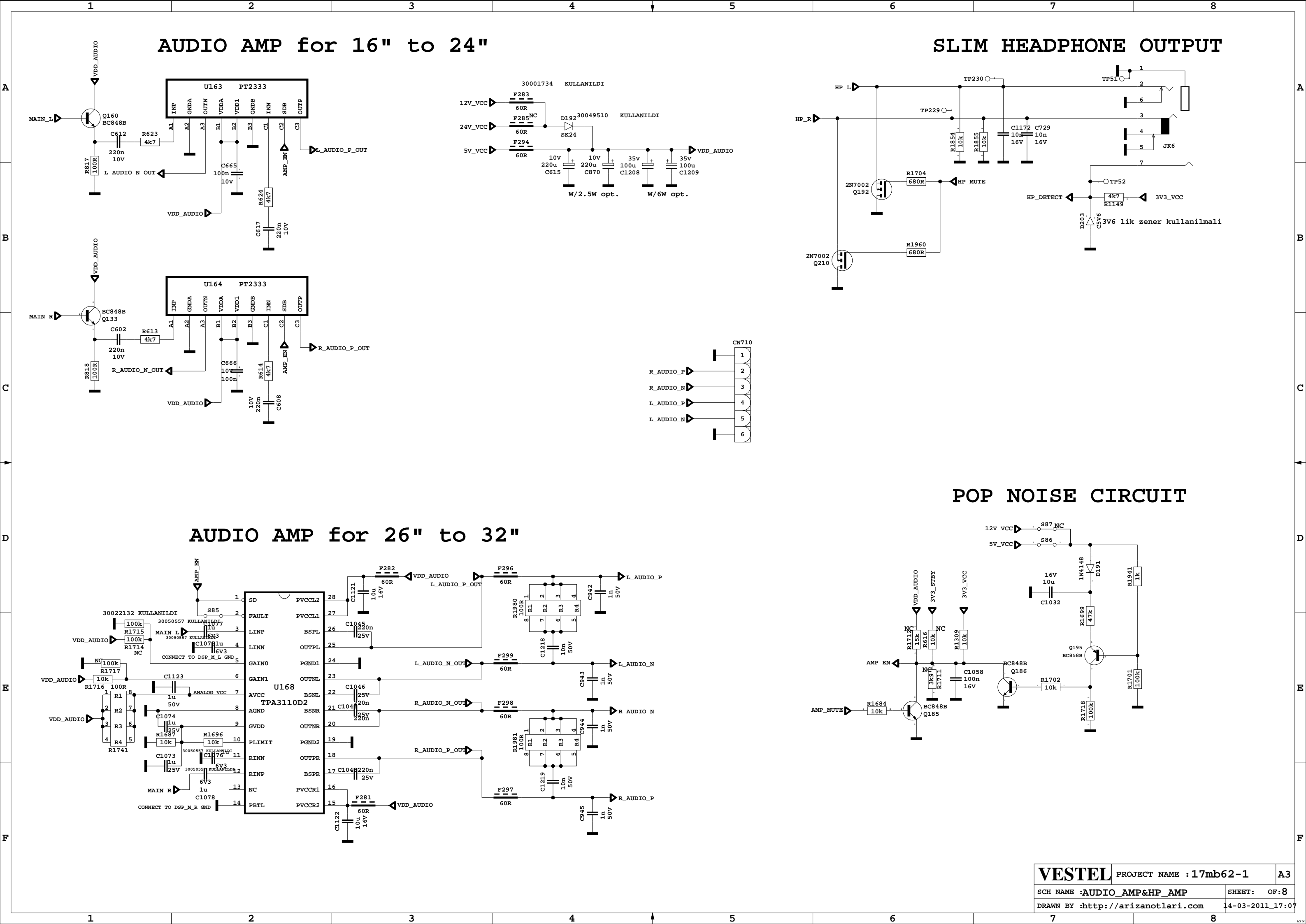
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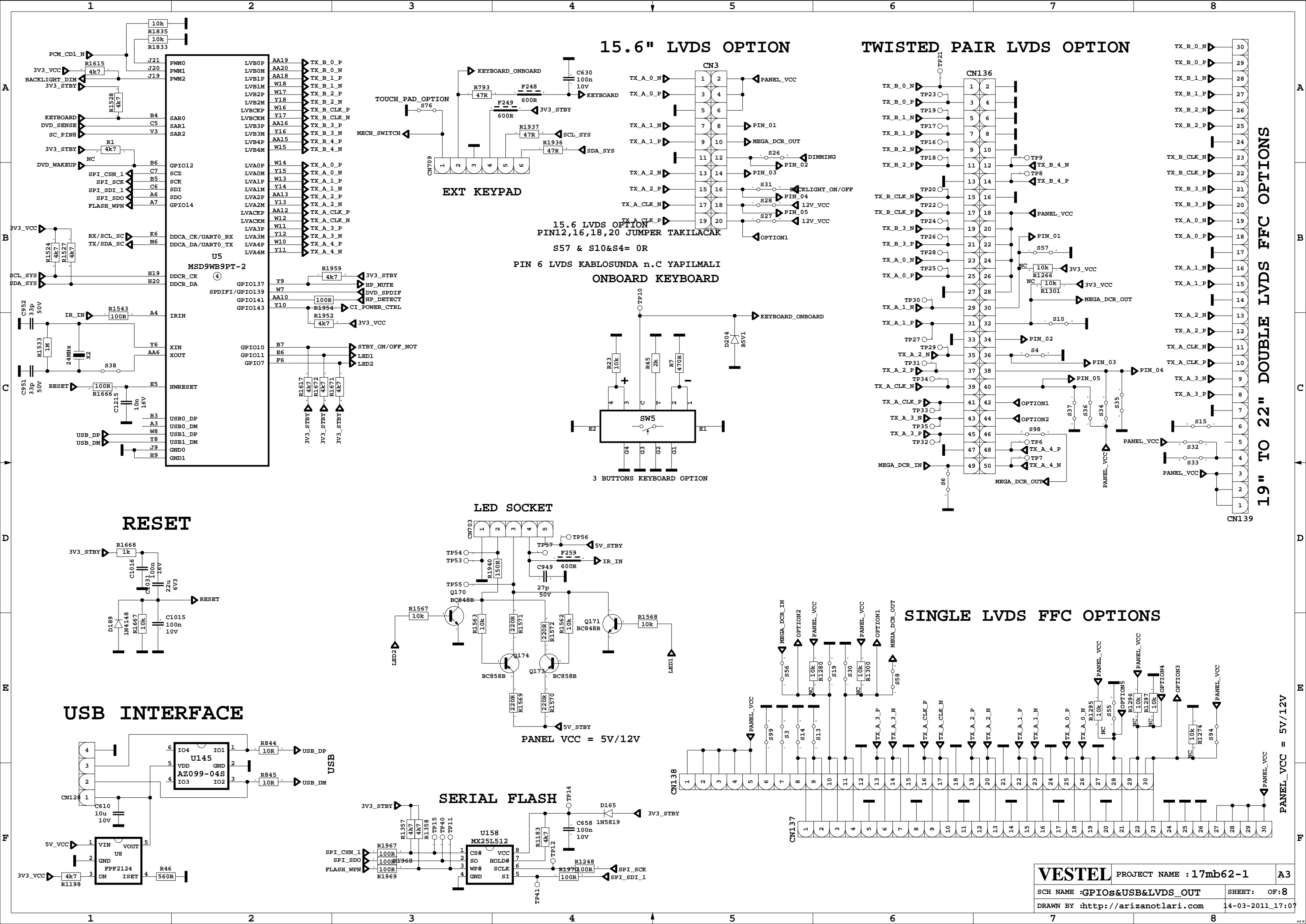


AUDIO AMP for 16" to 24"

SLIM HEADPHONE OUTPUT

AUDIO AMP for 26" to 32"

[illegible]



W_ADAPTER

JK9

TP36

TP239

Q1 FDC604P

Q2 FDC642P

FS2 7A/32VDC

NC

CCFL INVERTER SOCKET

CN705

12V_INV

TP37

TP39

DIMMING

BACKLIGHT_ON/OFF

TP45

TP46

The image contains three circuit diagrams labeled SW1, DC/DC2, and SW3.

- SW1:** A MOSFET control circuit. It features a 12V_STBY input connected to a 220nF capacitor (C1049) and a 25V MOSFET (Q182, BC848B) through a 10k resistor (R1692). The MOSFET's gate is also connected to a 33k resistor (R1733) and a 47R resistor (R1724) to ground. The MOSFET's drain is connected to a 33k resistor (R1732) and a 4A, 7A/32VDC fuse (FS5) to a 12V_VCC output.
- DC/DC2:** A buck converter circuit using the MP1484 IC (U23). The input is 12V_STBY, connected to a 60R resistor (F286) and a 22uF capacitor (C1089). The IC's pins are connected as follows: BS (1) to 12V_STBY, SS (2) to 12V_STBY, IN (3) to 12V_STBY, EN (7) to 12V_STBY, SW (6) to 12V_STBY, COMP (5) to 50V, FB (4) to 50V, and GND (8) to 50V. The output is 3V3_STBY, connected to a 22k resistor (R1735) and a 22uF capacitor (C1097). The output filter consists of an inductor (L123, 15uH) and a capacitor (C1096, 22uF, 6V3).
- SW3:** A MOSFET control circuit. It features a 3V3_STBY input connected to a 220nF capacitor (C1048) and a 25V MOSFET (Q183, BC848B) through a 10k resistor (R1697). The MOSFET's gate is also connected to a 33k resistor (R1731) and a 47R resistor (R1723) to ground. The MOSFET's drain is connected to a 33k resistor (R1730) and a 2A, 7A/32VDC fuse (FS4) to a 3V3_VCC output.

The schematic diagram shows the LM1117 voltage regulator circuit. The input is connected to 5V_VCC through a 60R resistor. The output is connected to 2V5_VCC. The LM1117 is configured with IN, OUT, GND, and VOUT pins. The output is connected to a 3V3_VCC output.

[illegible]

The schematic diagram illustrates the MP1484 buck converter circuit. The input stage features a 12V_VCC source connected to the IN pin of the MP1484 (U173) through a 60R resistor (F273) and a 22uF, 16V capacitor (C1085). The MP1484 is configured with a feedback network consisting of a 10k resistor (R1691) and a 3k9 resistor (R1710) connected to the FB pin, and a 39k resistor (R1686) connected to the COMP pin. The output filter includes an inductor L117 (10uH) and a 22uF, 6V3 capacitor (C1087). The output is connected to a load (TP231) and a 12V_VCC output. The circuit is powered by a 12V_VCC input and a 50V input source.