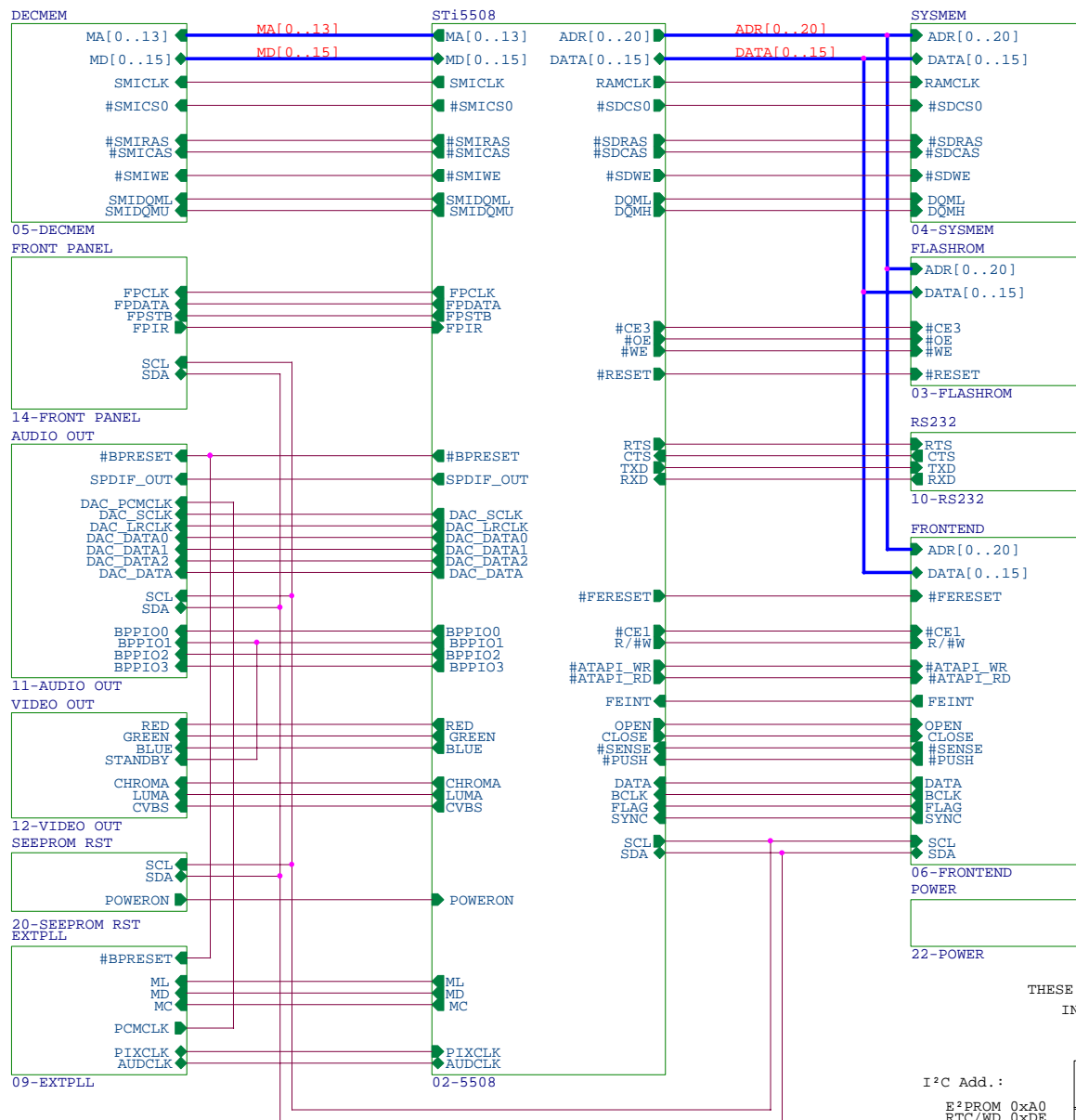


Production - STi5508/80

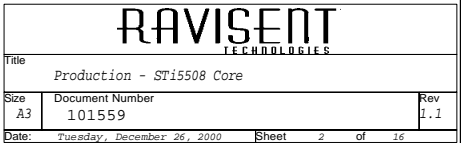
REV	DESCRIPTION	APPROVAL	DATE
1.0	INITIAL RELEASE		



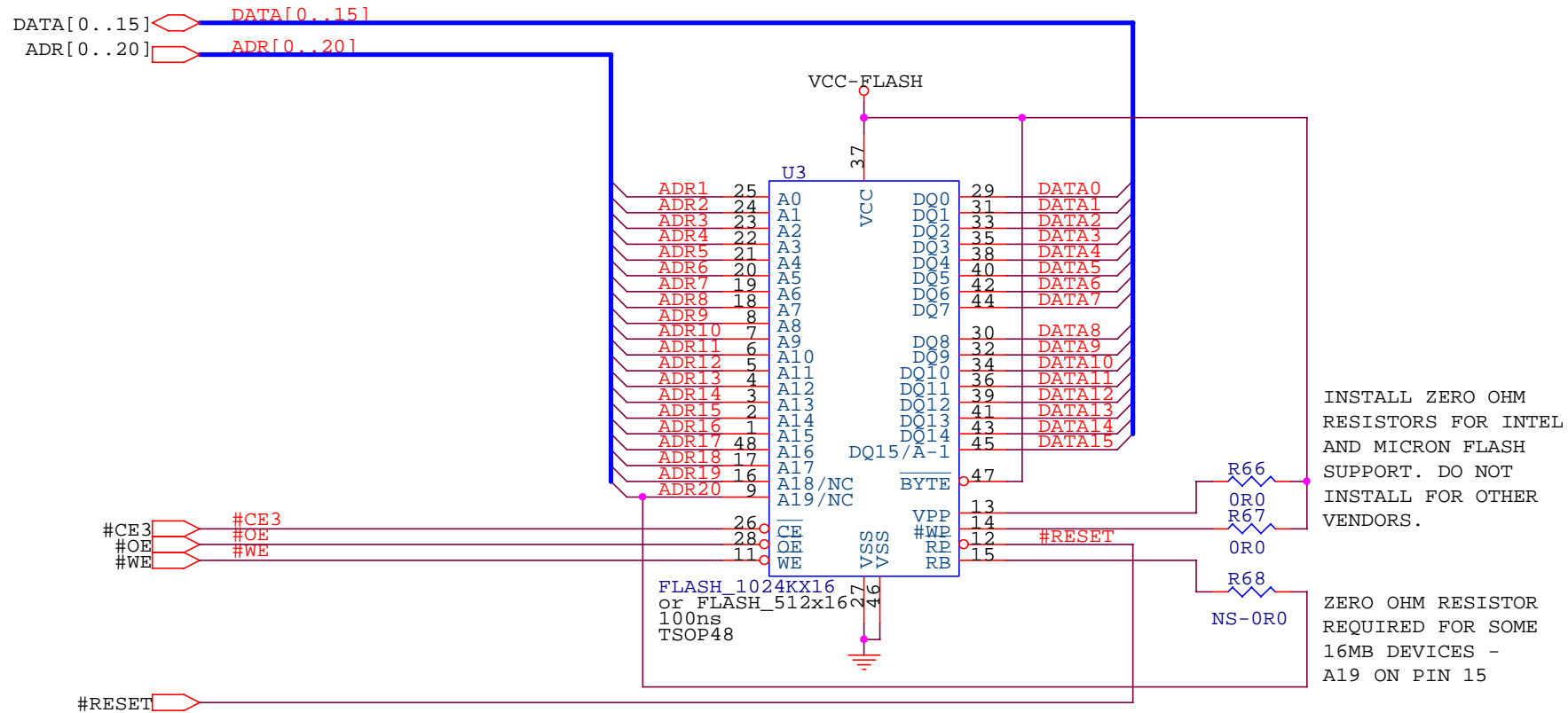
THESE SCHEMATICS ARE PROPRIETARY AND CONFIDENTIAL
INFORMATION OF RAVISENT Technologies Inc.
(c) RAVISENT Technologies Inc.

I²C Add.:
E²PROM 0xA0
RTC/WD 0xDE
NW-MEM 0xAE
TVMS02 0x30

RAVISENT TECHNOLOGIES		
Title Production - Overview of Decoder Board		
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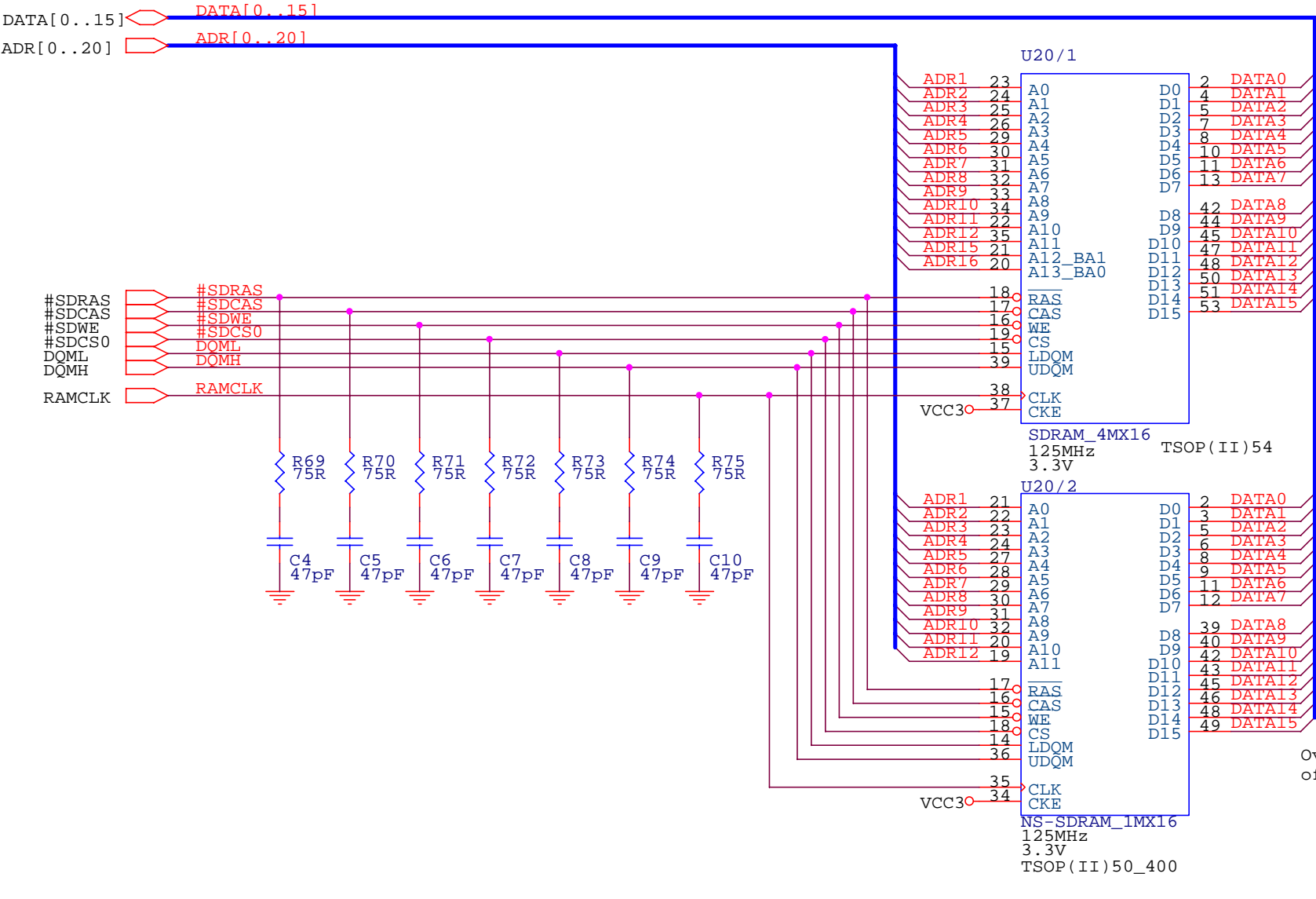
Firmware Flash ROM



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TECHNOLOGIES

Title		
Production - FLASH ROM Memory		
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Dedicated System Memory (Optional)



Overlap footprints
of /1 and /2 parts

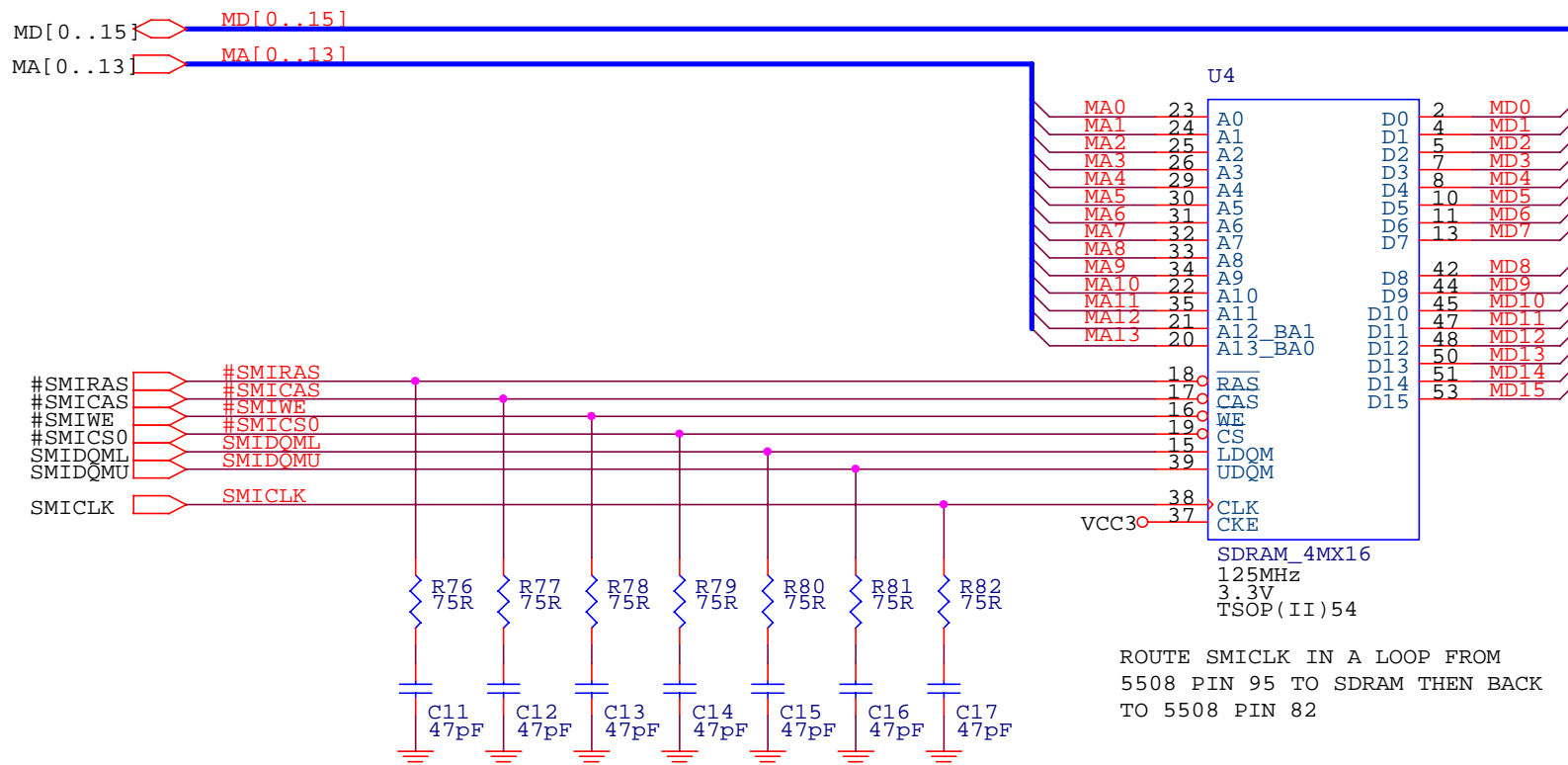


Note:
- place RC termination close to U5
- route SDCLK as short as possible
- 125MHz SDRAMs are required

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TECHNOLOGIES

Title		
Production - System DRAM Memory		
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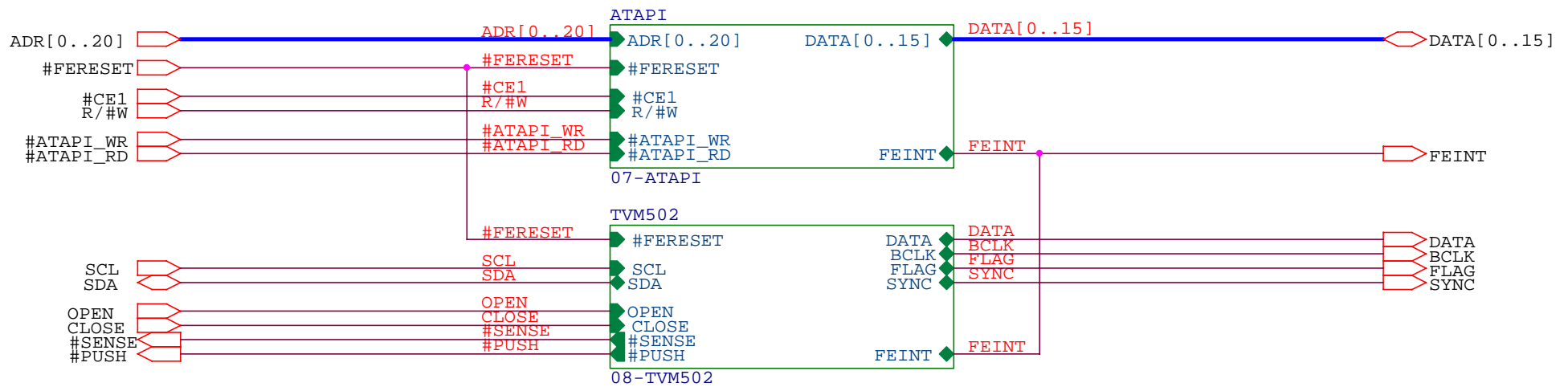
Decoder / SMI Memory



Note:

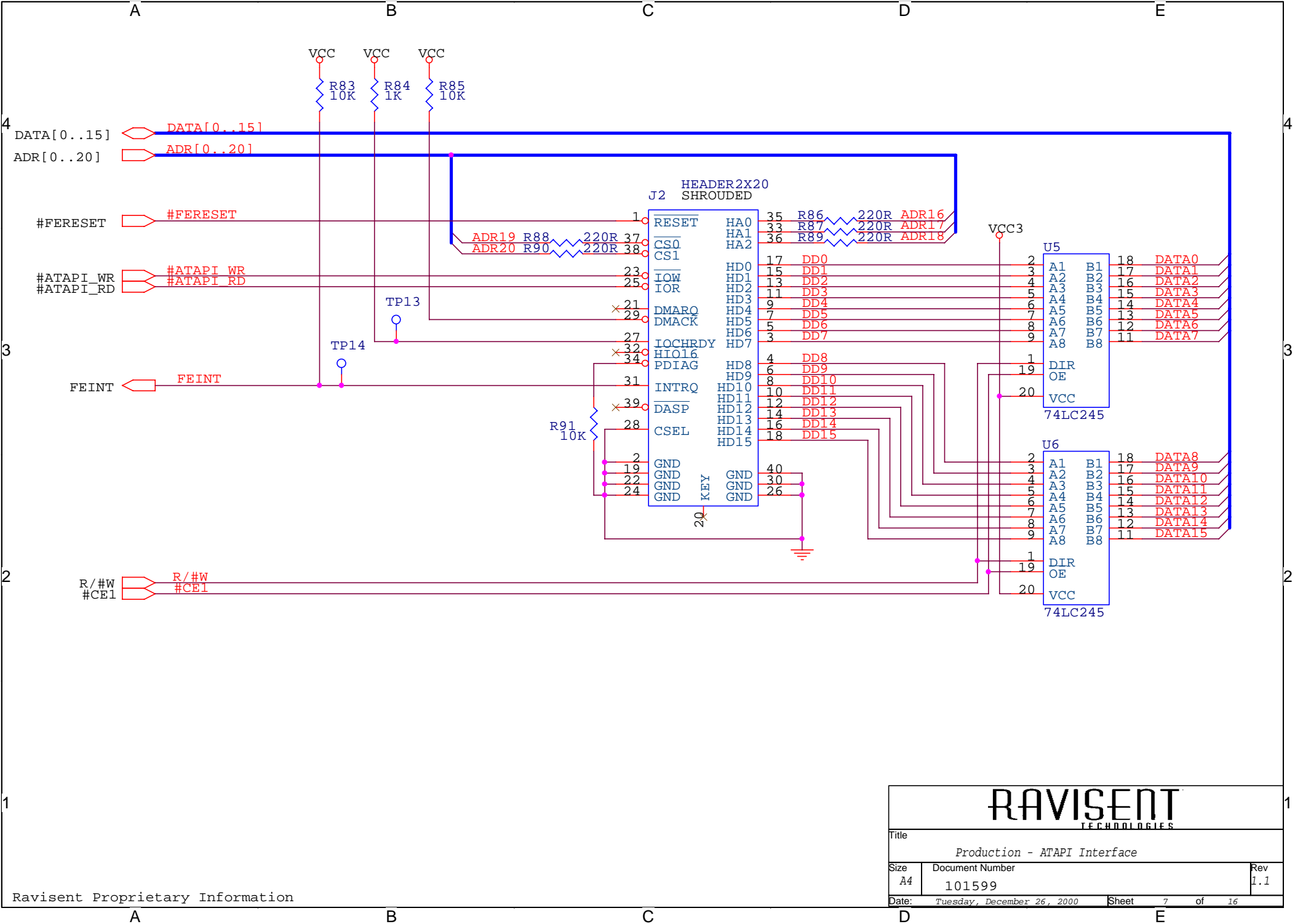
- place RC termination close to U6
- route SDCLK as short as possible
- 125MHz SDRAMs are required

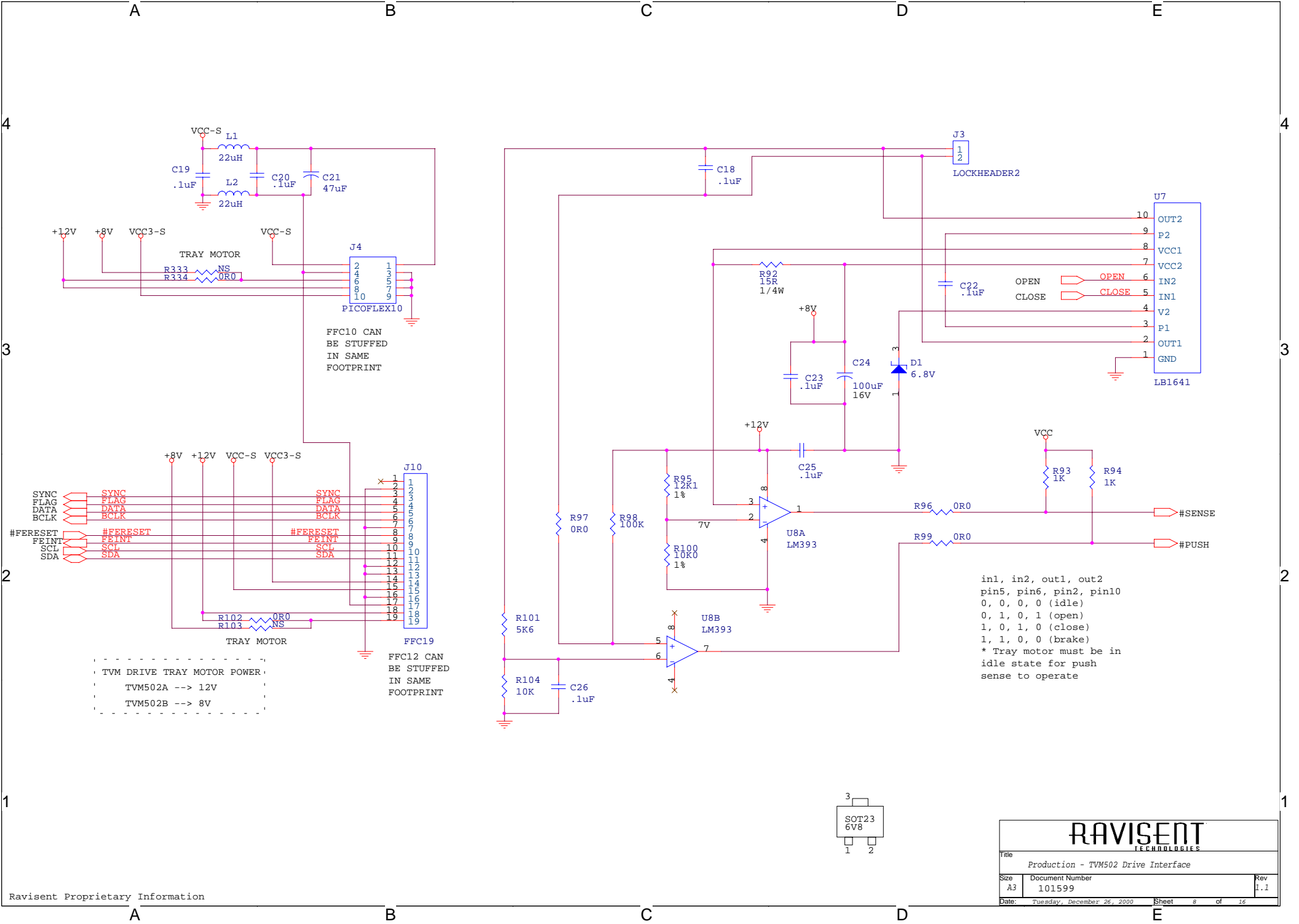
RAVISENT TECHNOLOGIES		
Title: Production - Decoder SDRAM Memory		
Size: A4	Document Number: 101599	Rev: 1.1
Date: Tuesday, December 26, 2000	Sheet: 5	of 16

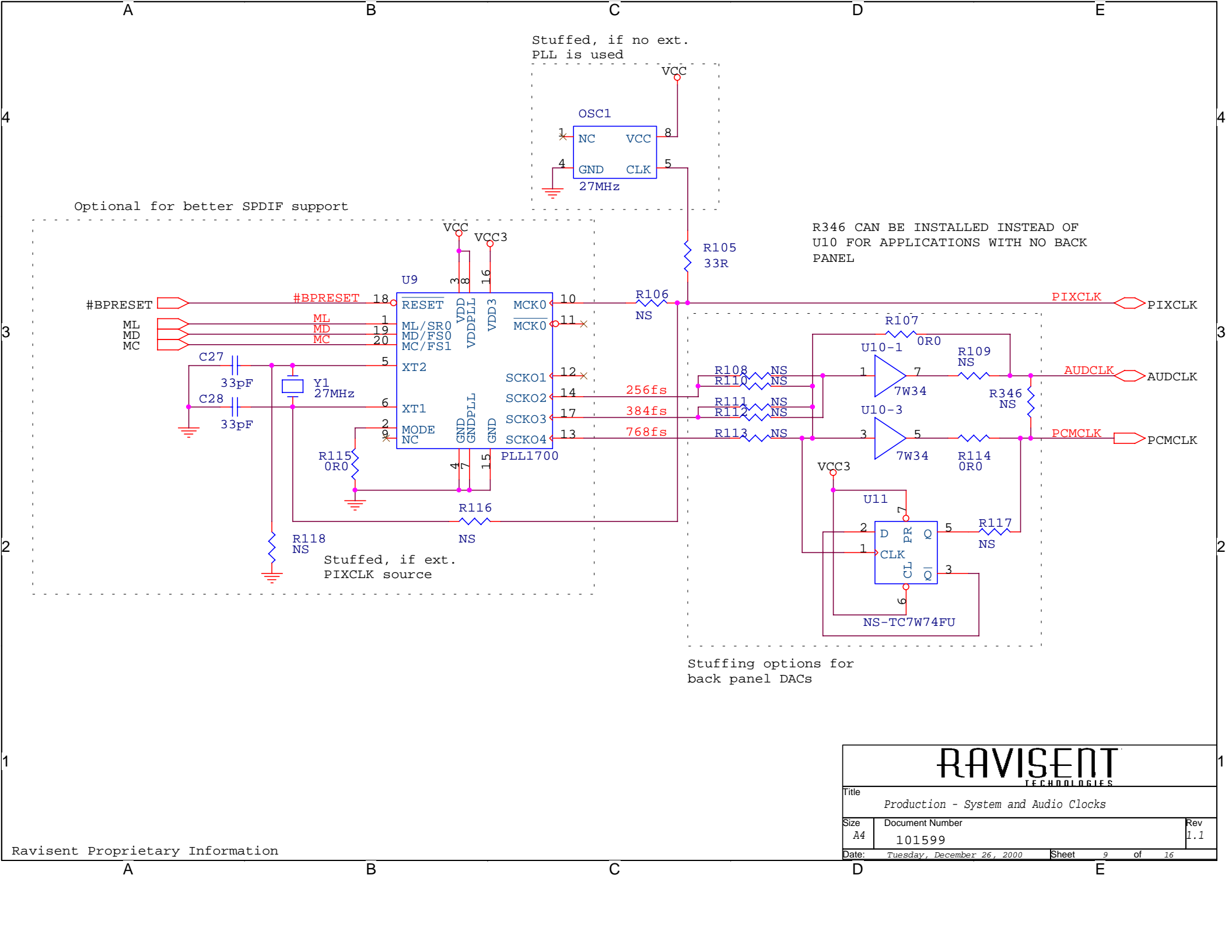


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TECHNOLOGIES

Title		
Production - Front End Options		
Size	Document Number	Rev
A4	101599	1.1
Date:	Tuesday, December 26, 2000	Sheet 6 of 16

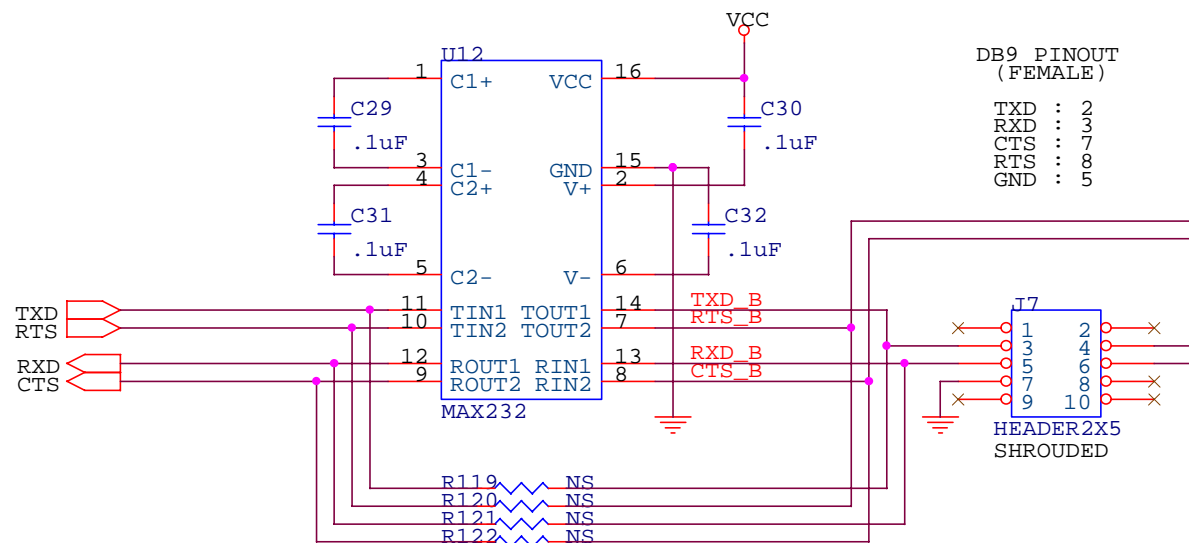






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TECHNOLOGIES

Title		
Production - System and Audio Clocks		
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BACK PANEL PIO FUNCTIONS

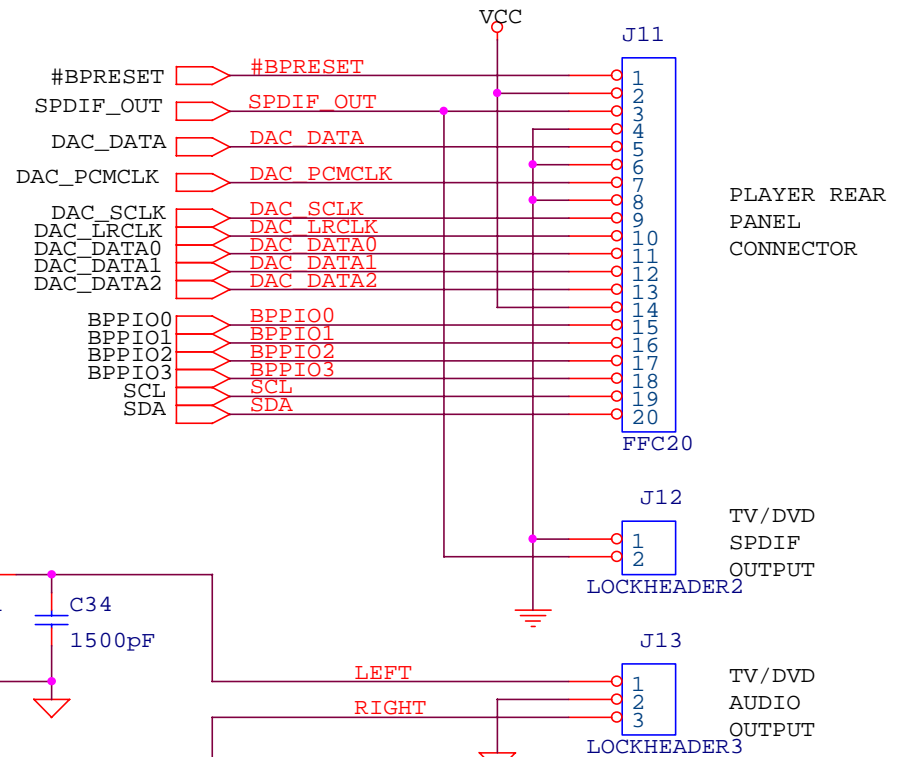
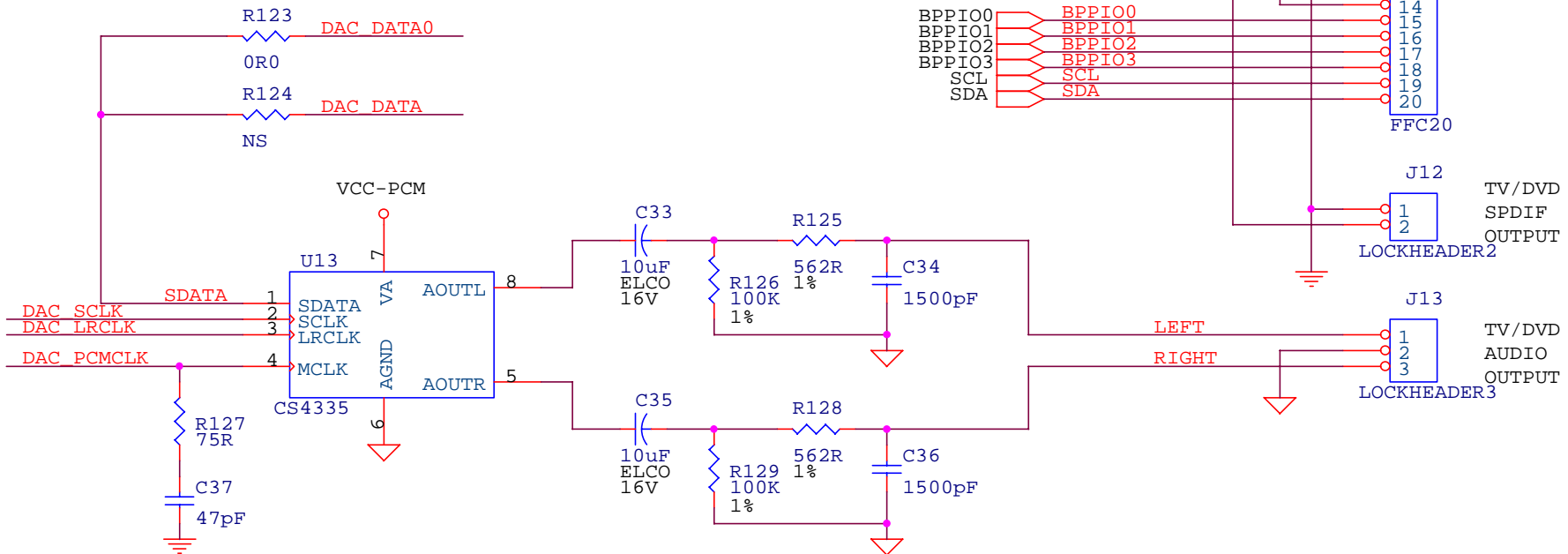
SIGNAL HIGH/LOW

BPPIO0 -- 4:3/16:9

BPPIO1 -- POWER/STANDBY

BPPIO2 --

BPPIO3 --



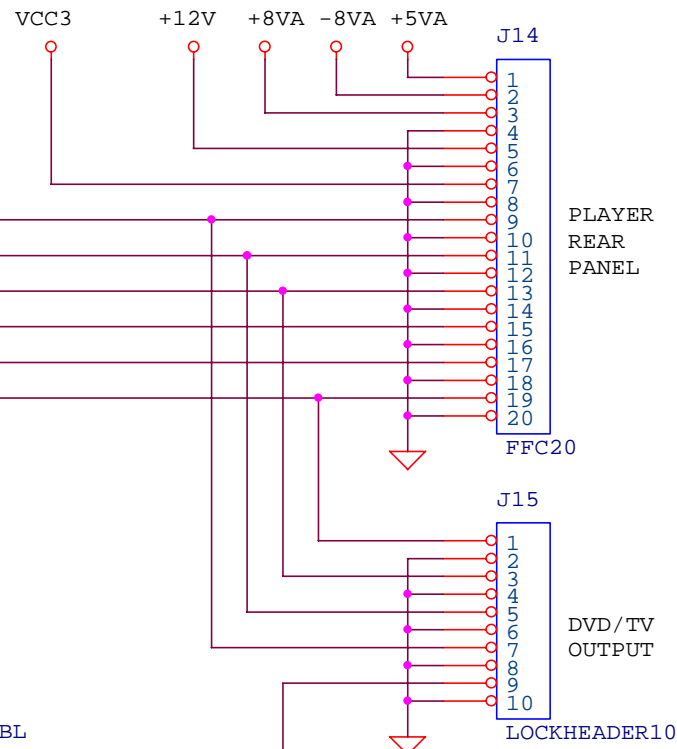
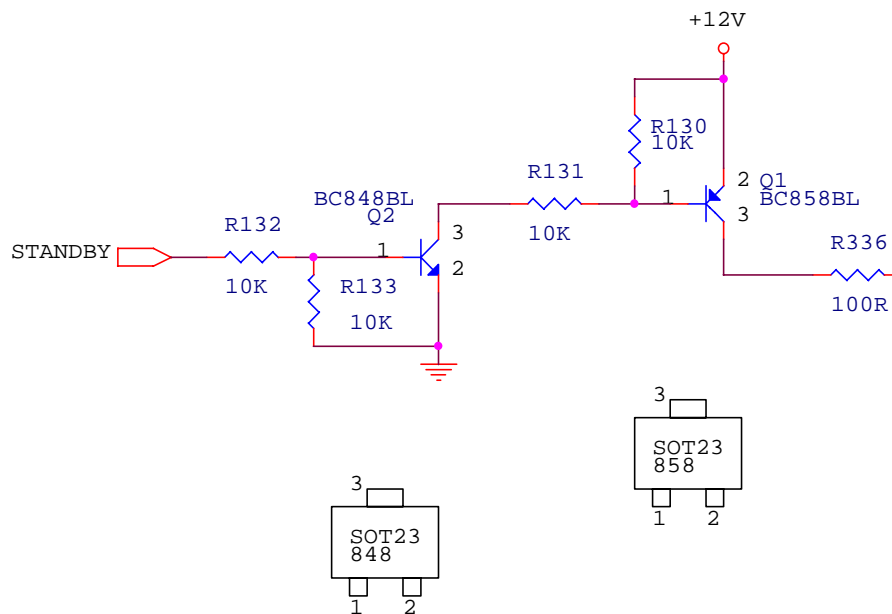
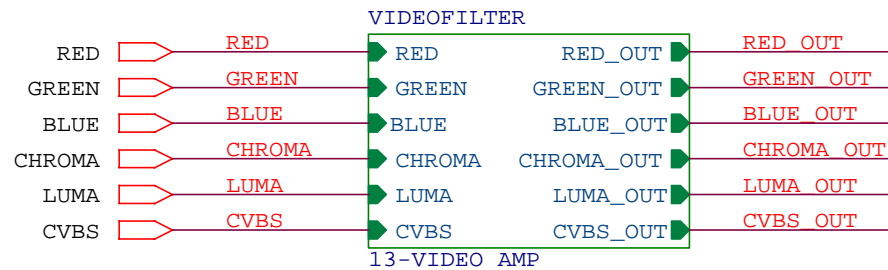
RAVISENT
TECHNOLOGIES

Title Production - Audio Output

Size A4 Document Number 101599

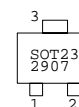
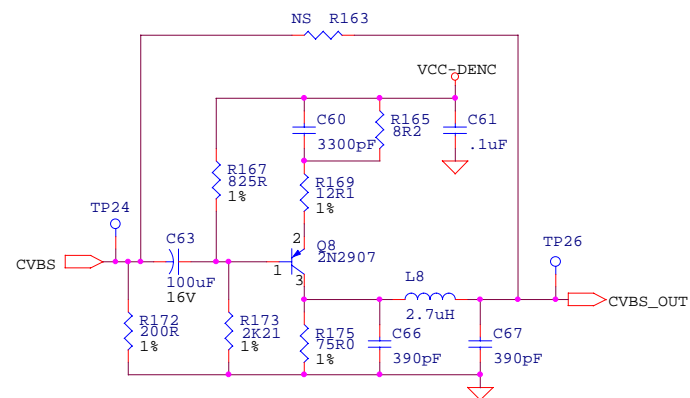
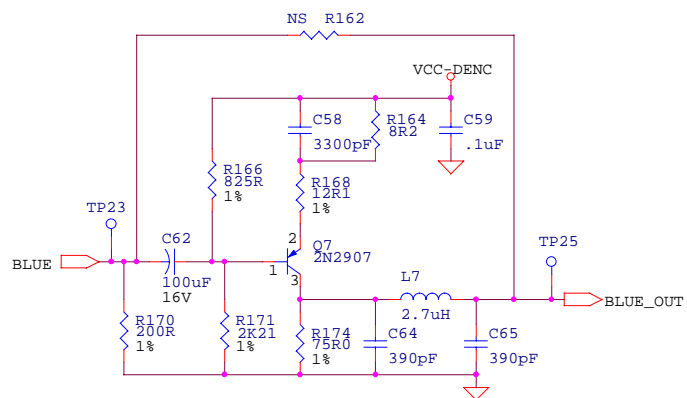
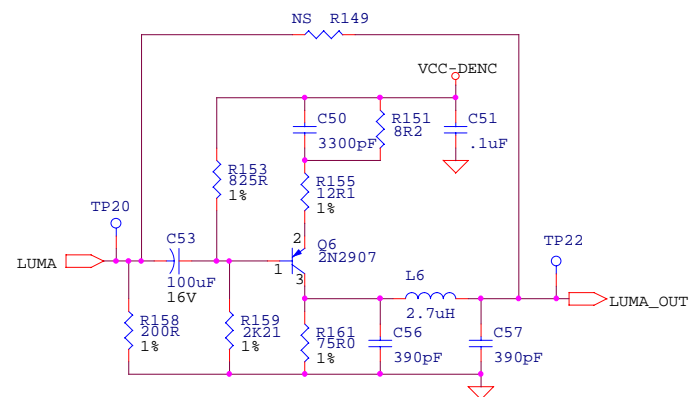
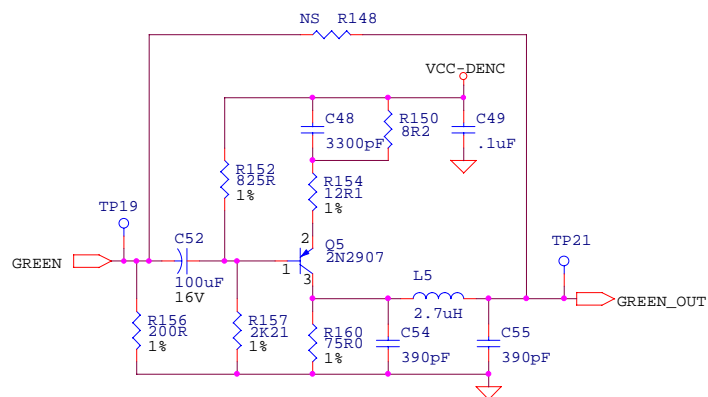
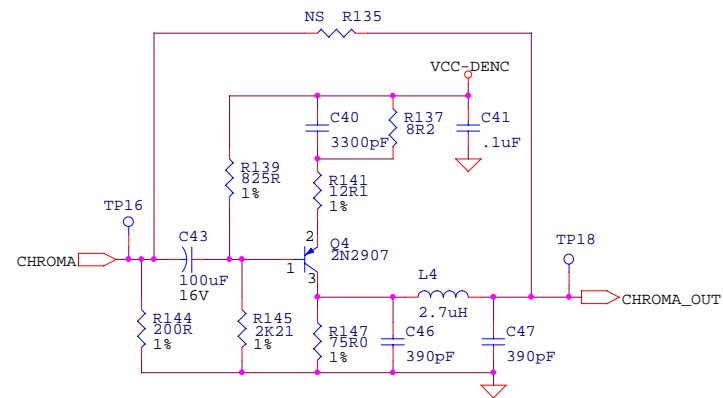
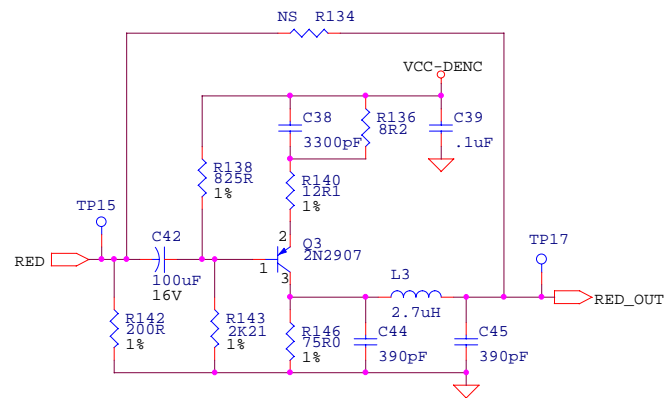
Rev 1.1

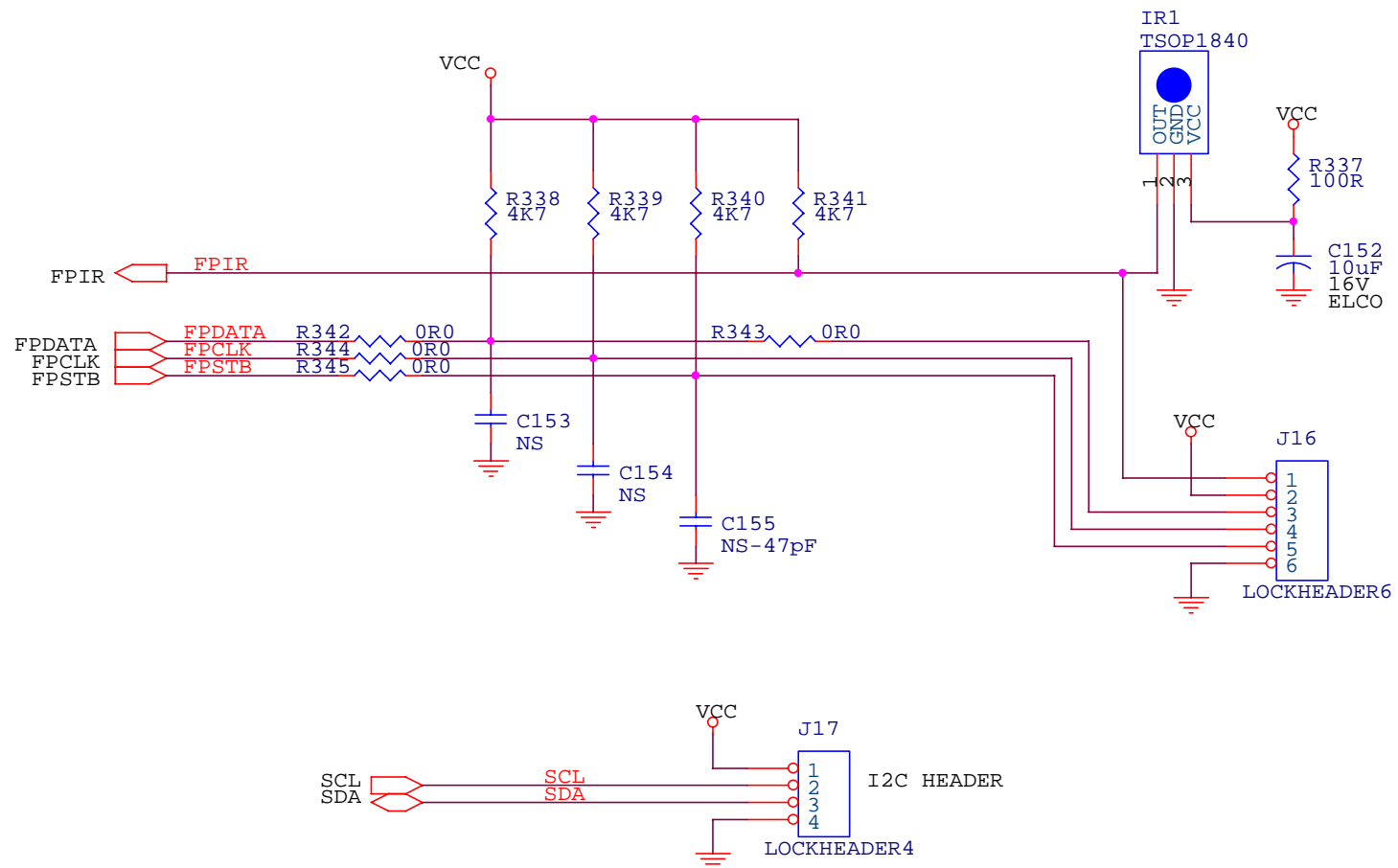
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TECHNOLOGIES

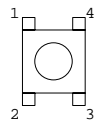
Title		
Production - Video Outputs		
Size A4	Document Number 101599	Rev 1.1
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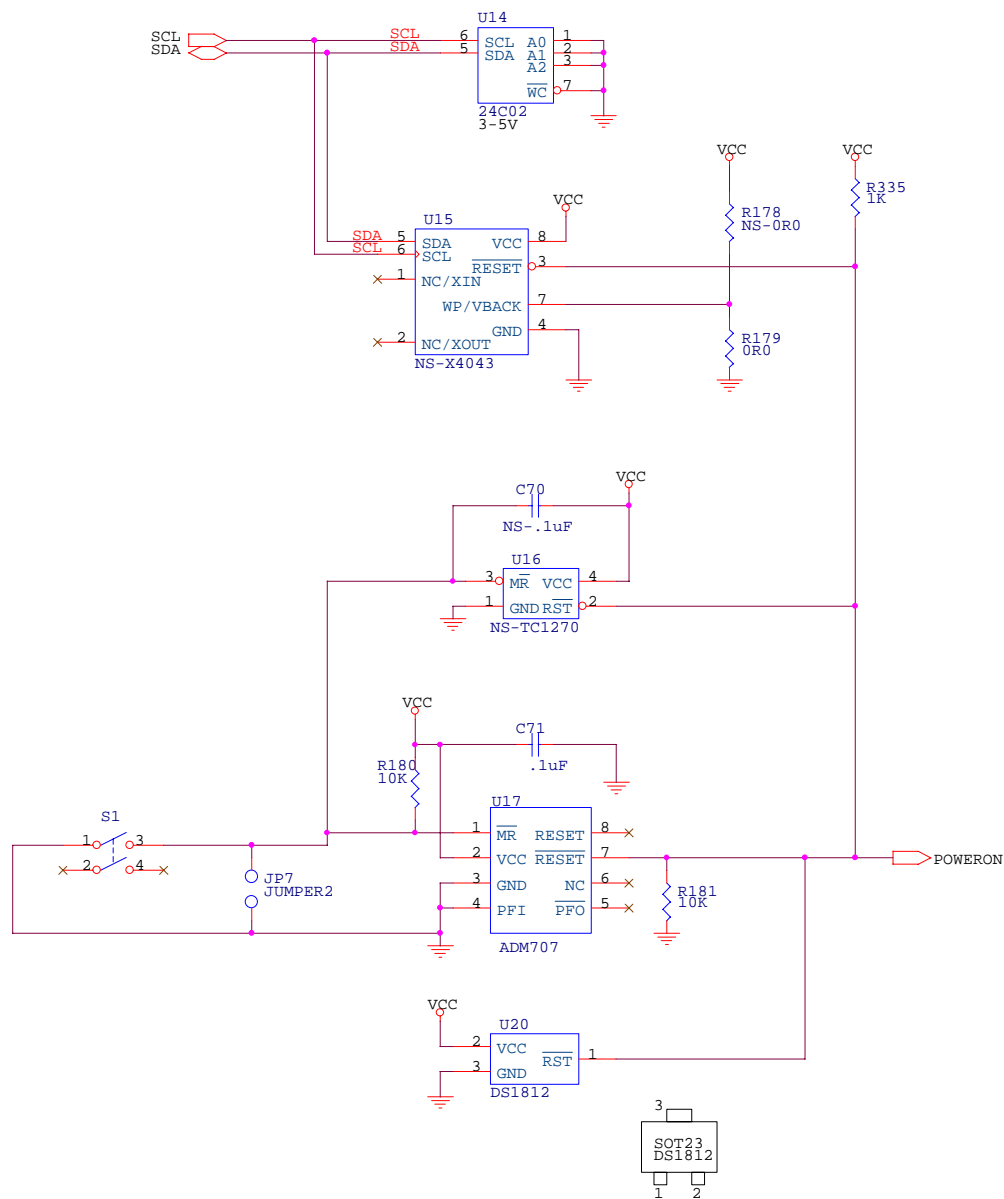


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Title			
Production - Front Panel			
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PUSHBUTTON SWITCH



PINS 1 AND 2 ARE CONNECTED
INTERNALLY
PINS 3 AND 4 ARE CONNECTED
INTERNALLY



RAVISENT TECHNOLOGIES		
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